

FIG I FIG II

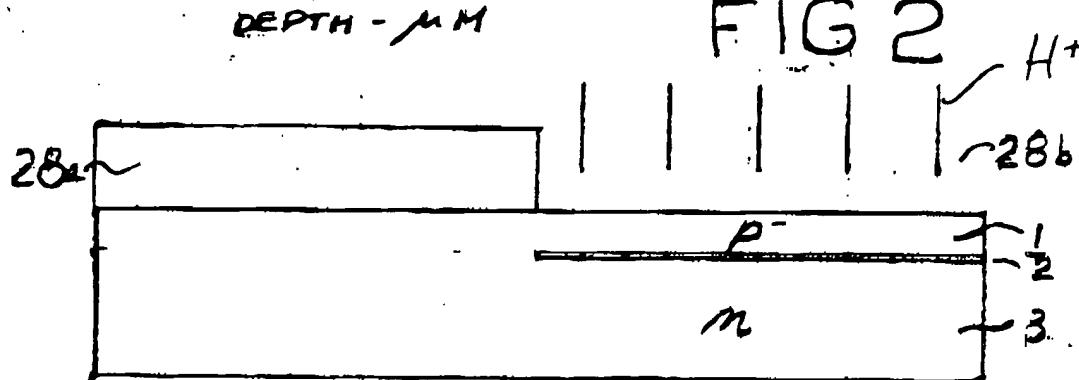
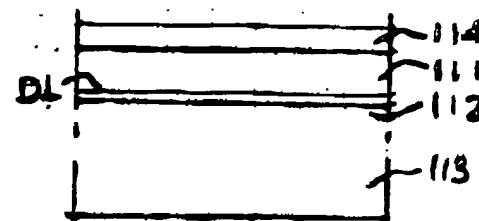


FIG 3 PMOS NMOS

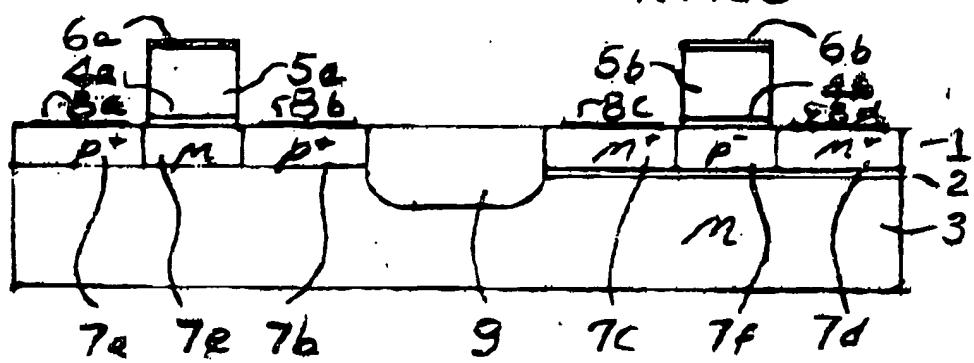


FIG 5

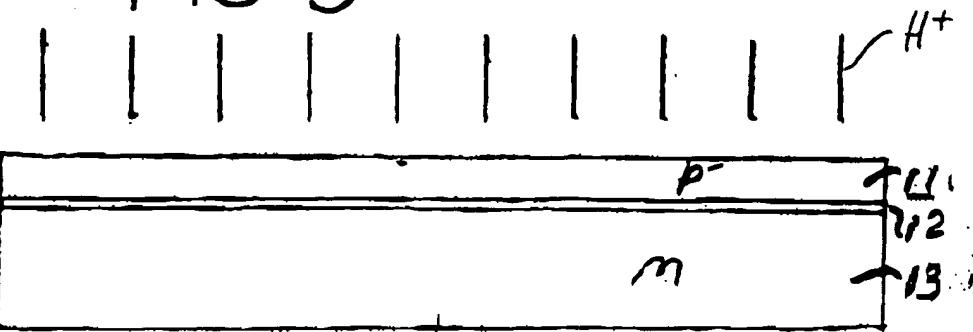


FIG 6

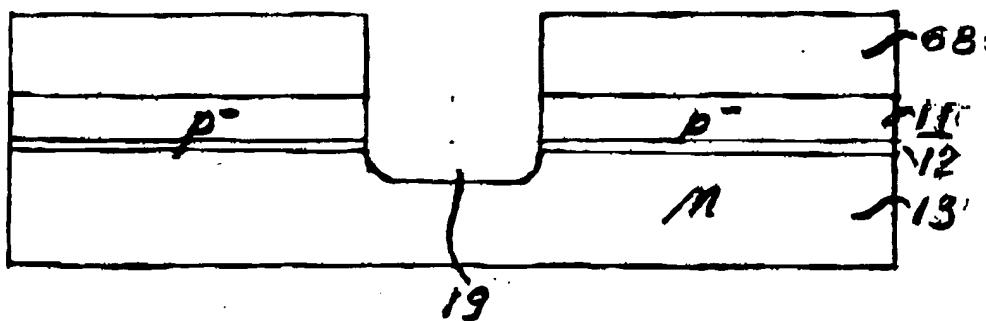
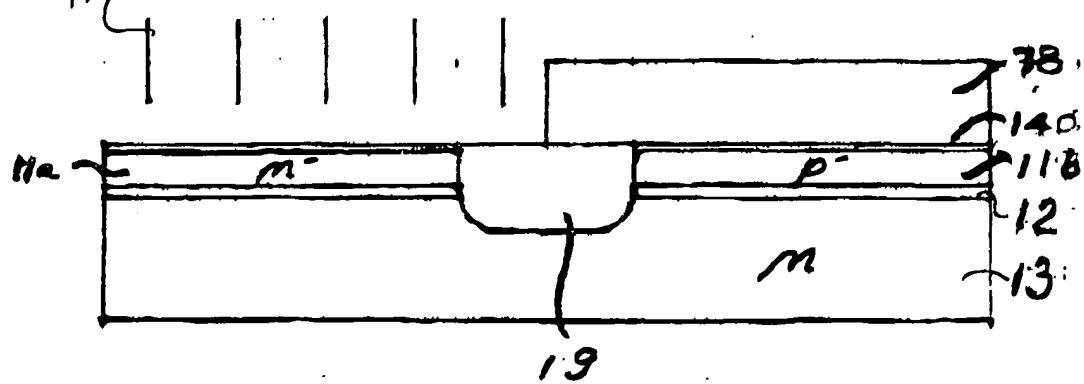
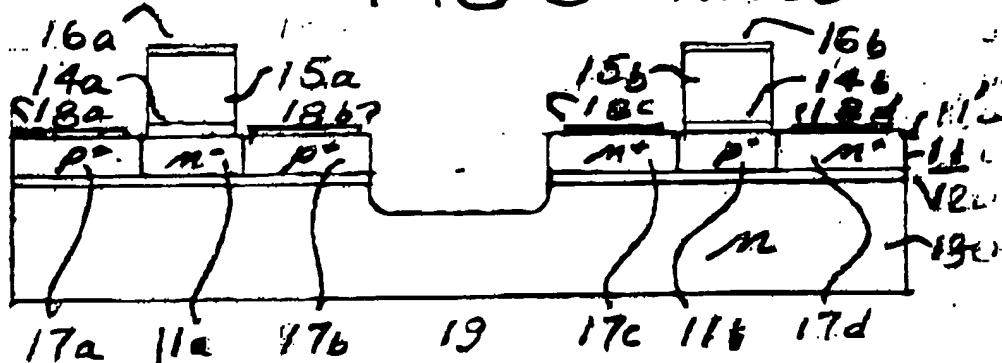


FIG 7



DMOS FIG 8 NMOS



DMOS FIG 9 NMOS

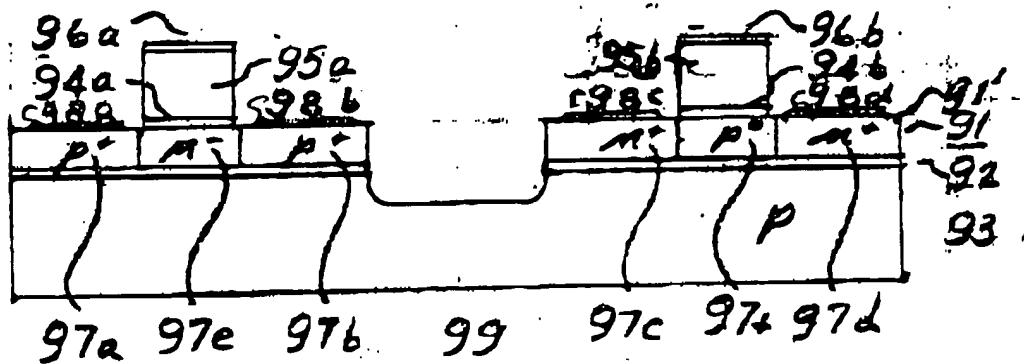


FIG 4

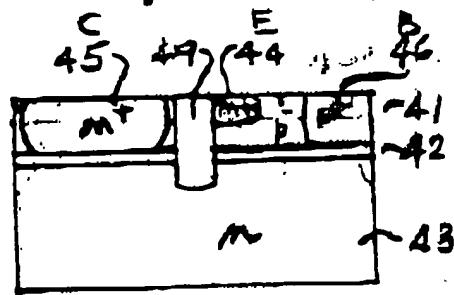


FIG 10

